

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Wayne F. Ellis et al.

Serial No.: 10/707,797

Filed: January 13, 2004

Title: Integrated Redundancy Architecture And Method for Providing Redundancy Allocation  
to an Embedded Memory System

Attorney Docket No.: BUR920030151US1  
(02016-00170)

Group Art Unit: 2117

Confirmation No.: 1796

Examiner: Guerrier Merant

Mail Stop 16  
Director of the USPTO  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 16, 2008

**REQUEST FOR REFUND**

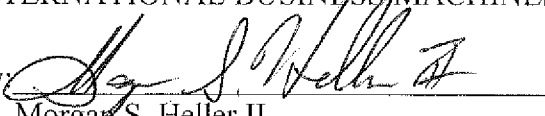
The Examiner in the above-referenced matter issued a Non-Final Office Action on May 28, 2008. The undersigned attorney mistakenly assumed the office action was final, and improperly filed a Request for Continued Examination on August 28, 2008.

Because the Request for Continued Examination was filed in error, it is respectfully requested that a refund in the amount of \$810.00 be issued to Deposit Account No. 09-0456.

Respectfully submitted,

INTERNATIONAL BUSINESS MACHINES CORP.

By:

A handwritten signature in dark ink, appearing to read "Morgan S. Heller II", is written over a horizontal line.

Morgan S. Heller II

Registration No. 44,756

DOWNS RACHLIN MARTIN PLLC

Tel: (802) 863-2375

Attorneys for Assignee

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